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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/637,621	08/11/2003	Akihiro Kojou	04329.3111	2103
22852 7590 06/29/2007 FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER LLP 901 NEW YORK AVENUE, NW WASHINGTON, DC 20001-4413			EXAMINER DU, THUAN N	
			ART UNIT 2116	PAPER NUMBER
			MAIL DATE 06/29/2007	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/637,621	Applicant(s) KOJOU ET AL.	
	Examiner Thuan N. Du	Art Unit 2116	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 20 April 2007.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-16 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. It is hereby acknowledged that the following papers have been received and placed of record in the file: Amendment (dated 4/20/07).
2. Claims 1-16 are presented for examination.
3. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

Claim Rejections - 35 USC § 102

4. Claims 1-16 are rejected under 35 U.S.C. 102(b) as being anticipated by Wang (U.S. Patent No. 6,041,413).
5. Regarding claim 1, Wang teaches an electronic device (12) comprising:
 - means (46) for holding information (logical high or logical low) representing whether activation of the electronic device is inhibited [Fig. 3; col. 8, lines 8-20];
 - a switch (main power switch) which, when turned on, inhibits activation of the electronic device (when the main power switch is turned on, logical high always tied directly with PS-ON, therefore, the power supply always maintained at its OFF state) [col. 8, lines 16-20, 29-36]; and
 - means for executing activation or activation inhibition the electronic device (firmware) on the basis of the information stored in the holding means and which is set by the switch (logical signal always set at high level when the main power switch is turned on) when an activation instruction is generated from an application program (password program) of the electronic device [col. 7, line 66 to col. 8, line 20].

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6. Regarding claim 2, Wang teaches that the switch is arranged in a housing of the electronic device [col. 8, lines 29-30].
7. Regarding claim 3, Wang teaches that the system includes a sub-processor (controller 40) different from a main processor (system CPU) [col. 5, lines 37-38] arranged in the electronic device.
8. Regarding claim 4, Wang teaches that the system includes a sub-processor (controller 40) different from a main processor (system CPU) [col. 5, lines 37-38] arranged in the electronic device, and the holding means (46) is arranged in the sub-processor [Fig. 3].
9. Regarding claim 5, Wang teaches that the switch designates inhibition even when the electronic device is OFF [col. 8, lines 29-36].
10. Regarding claim 6, Wang teaches that the system further comprises a power supply controller (PSCL 46) that manages a power supply of the electronic device [col. 8, lines 9-20], and a power supply request is issued to the power supply controller when the activation instruction is received and activation of the electronic device is permitted [col. 8, lines 5-7].
11. Regarding claims 7-16, they do not teach or further define over the limitations recited in claims 1-6 above. Therefore, claims 7-16 are also anticipated by Wang for the same reasons set forth in claims 1-6.

Response to Arguments

12. In response to applicant's argument that Wang does not teach or suggest "information representing whether activation of the electronic device is inhibited," examiner respectfully disagrees. Wang discloses that when PS-ON = 1, system in OFF state; and when PS-ON = 0,

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system in ON state [col. 8, lines 16-20]. At col. 8, lines 29-36, Wang discloses a NAND gate used to output the PS-ON signal. The inputs of the NAND gate are the PM (password match) signal and the MSW (main power switch) signal. The PS-ON=0 (to turn on the system) only when both PM=1 (password matched) and MSW=1 (main power switch ON). When MSW=1 (main power switch ON) and PM=0 (password does not match), the output PS-ON=1 (system remains OFF). As such, the PS-ON is always set at high-level (1) at the time of the main power switch is turned on. Therefore, the logic level (0 or 1) in Wang is the information as claimed.

Conclusion

13. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

14. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan N. Du whose telephone number is (571) 272-3673. The examiner can normally be reached on Monday-Friday: 7:30 am - 4:00 pm, EST.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached at (571) 272-3676.

Central TC telephone number is (571) 272-2100.

The fax number for the organization is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll free).

TD
June 20, 2007



THUAN N. DU
PRIMARY EXAMINER